

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Docket No.: 1418.1007

#6/Amend.B
Q7024
7/23/02
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JUL 12 2002
TC 2800 MAIL ROOM

In re the Application of:

Hideharu MATSUSHITA et al.

Serial No. 09/942,643

Group Art Unit: 2825

Confirmation No. 8806

Filed: August 31, 2001

Examiner: Leigh M. Garbowski

For: PRINTED CIRCUIT BOARD DESIGN SYSTEM

AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This is in response to the Office Action mailed April 10, 2002, and having a period for response set to expire on July 10, 2002.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested. No new matter has been added.

IN THE CLAIMS:

Please REPLACE claim 1 and ADD new claims 6-10 in accordance with the following:

1. (ONCE AMENDED) A printed circuit board design system for generating a 3D model of a printed circuit board which mounts a component on a printed board, and performing with a three-dimensional CAD system a mounting design which includes a cabinet, the system comprising:

a converter for converting the printed circuit board into one or more models based on attributes preliminarily added to the component.

6. (NEW) A method for generating a 3D model of a printed circuit board which mounts a component on a printed board, and performing with a three-dimensional CAD system

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